Xtables2: Love for blobs

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- Combined with compat support, there are now *seven* formats to support in the kernel
- A big itch to scratch.

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- Combined with compat support, there are now *eight* formats to support in the kernel
- Eight itches to scrub.

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A protocol-independent format

- Rule tree without protocol-specific parts in it, to be used by and for all protocol handlers
- Translatation from and to input formats on-the-fly, i. e. during S0_SET_REPLACE/etc.

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- \Rightarrow Led to Xtables2

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Developments

 $SSA/LL^{1,2}$ style:

- "proto1": initial submission on 2009-Aug-04 for v2.6.31-rc (103 patches)
- busy dealing with cleanups: 46/103

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- busy dealing with cleanups: 46/103
- "proto2": partial set posted on 2010-Jun-04 for v2.6.35-rc (33 patches, and a nasty surprise)
- "proto3": simple rebase for v2.6.36-rc for better comparison with the upcoming proto4

PCR style:

• "proto4": xt2 using packed-chain rulesets, for v2.6.36-rc

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Section TOC

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Chosen data layout

- Linked lists allow for "easy manipulation" of the ruleset
- Small-scale allocations (SSA) are more easily satisfiable.

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Chosen data layout

- Linked lists allow for "easy manipulation" of the ruleset
- Small-scale allocations (SSA) are more easily satisfiable.
- Prototype: Translators work nicely, and with a bit of macro magic, eliminated 40% of LOC from the {ip,ip6,arp} combo.

Ruleset

• Just a simple ruleset that would be large enough so that wall time is visible

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Ruleset

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Just struct ip6t_entry, but lots of them

- -A \$chain -s ::1 -d ::1
 - no extensions, just struct ip6t entry \times 1000 rules \times 100 chains reachable from INPUT (OUTPUT is left empty)

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 - no extensions, just struct ip6t_entry \times 1000 rules \times 100 chains reachable from INPUT (OUTPUT is left empty)
 - 100,202 rules (100,000 base rules + 100 calls + 100 implicit invisible RETURNs converted from Xt1 + 2 implicit Xt1 RETURNs from base chains)
 - \approx 20 MB in packed form

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Jesper has down-to-earth rulesets:

67,892 visible rules in 18,329 chains: rule density distribution

> summary(data) Min. 1st Qu. Median Mean 3rd Qu. Max. 1.000 1.000 2.000 3.745 4.000 119.000

- Packed size is 16,866,200 bytes
- Design: fanned tree, only \approx 53 rules executed per packet ٠

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Jesper has down-to-earth rulesets:

662,160 visible rules in 151,426 chains: rule density distribution

> summary(data) Min. 1st Qu. Median Mean 3rd Qu. Max. 1,000 1,000 4,000 4,477 4,000 144,000

- Packed size is 156,258,112 bytes
- Design: fanned tree, only \approx 77 rules executed per packet
- Low rule density sounds like management overhead need to keep that in mind for later

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100,000× struct ip6t_entry

-A mychain\$i -s ::1 -d ::1

• Earlier tests with ping6 -f were flawed.

Testing proto2 ping6 -fqc 500 -i .001 localhost

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100,000× struct ip6t entry

- -A mychain\$i -s ::1 -d ::1
 - Earlier tests with ping6 -f were flawed.

Testing proto2 ping6 -fqc 500 -i .001 localhost

- Without rules, this gives 500 ms total execution time: packet handling is quick, ping is just waiting for the intervals to expire.
- -i .001 made sure that (with rules) no packets were reported dropped
- With rules, this goes up: once it starts going above 500 ms, we know packet processing takes longer than the 1 ms interval.

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- So-gathered statistics showed an execution time expansion of 4.30 \times (xt1: 3500 ms \rightarrow proto2: 15000 msec)
- "Linked lists no good?"

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- So-gathered statistics showed an execution time expansion of 4.30 \times (xt1: 3500 ms \rightarrow proto2: 15000 msec)
- "Linked lists no good?"
- Using ping this way was flawed... ping handles packets asynchronously when using -f
- Let's reset.

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Testing proto3 with revised command

ping6 -Ac 500 ::1

• Observing ping's RTT statistics rather than execution time

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Testing proto3 with revised command

ping6 -Ac 500 ::1

- Observing ping's RTT statistics rather than execution time
- Additionally, I sampled the CPU cycle counter around xt2_do_table and the ematch loop in xt2_do_actions
- \Rightarrow much more consistent results

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- Expansion factor: $2.80 \times$ (xt1: 40.477 ms \rightarrow proto3: 113.424 ms)
- Increase expected (being a pessimist), but this much still blew everything

³http://events.linuxfoundation.org/2010/linuxcon-japan/rowand -Identifying Embedded Real-Time Latency Issues: I-Cache and Locks

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- Speculation: lots of D-cache misses³ due to the objects being "spread out" in memory
- Use of kmem_cache pools for objects of constant size (table, chain and rule list heads) showed no improvement
- And then there was memory...

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Memory usage

Previously, with a blob:

• 1 vmalloc'd object of ${\approx}20~\text{MB}$

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Memory usage

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Now, split allocations...?

- SL*B has to housekeep an 1,002,111 extra kmalloc'd objects now
 - $1 \times \text{ struct xt2_table}$
 - $100 \times \text{ struct xt2_chains}$
 - $100,201 \times \text{ struct } xt2_rules$
 - 100,201× struct xt2_entry_match for "ipv6"
 - 100,201× struct ip6t_ip6 for "ipv6"
 - 200,402× struct xt2_entry_match for "quota"
 - 200,402× struct xt_quota for "quota"
 - 200,402× struct xt_quota_priv for "quota"
 - $100,201 \times \text{struct xt2}_\text{entry}_\text{target}$ for implicit CONTINUE
 - This is of course the other end of the two extremes.

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- Memory usage increase of 2.7× (i586). /proc/slabinfo:
 - \approx 900,000 \times size-32
 - $\approx 100,000 \times \text{size-192}$
 - $\bullet~$ 48 MB, plus some housekeeping, for a total of $\approx\!53$ MB

Layman's observation					
<pre># free; ip6tables-restore bigrules; free</pre>					
	used	free			
-/+ buffers/cache:	34056	1002172			
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 \Rightarrow Small scattered allocations are a no-go.

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Section TOC

- Evaluation of rules: we want no scattered allocs
- Housekeeping: we want few allocs

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- Housekeeping: we want few allocs
- Original iptables design decision pays off (Harald was right all along!)
 - packed ruleset allows for streaming reads
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Need to find ways to make working with them easier

- A good API is half the job
- Algorithms to keep the time cost of updating rulesets in-place low

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About APIs

• Opaque macros/functions gone too opaque

IP6T_MATCH_ITERATE

xt_ematch_foreach

- Implementation is also much friendlier to long-term maintainers
- xt_ematch_foreach is KISS and may save function call overhead

IP6T_MATCH_ITERATE

xt_ematch_foreach

```
#define xt_ematch_foreach(pos, entry) \
    for (pos = entry->elems; \
        pos < entry + entry->target_offset; \
        pos = pos + pos->u.match_size)
```

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• Xt1 blob rules refer to chains (when jumping) by their absolute offset in the blob (i. e. bytes from the start of the blob)

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- Insertion or deletion of a chain/rule in a blob shifts the offset of all subsequent chains
- Requires updating the chain offsets of all jumping rules
- With k rules already loaded, that is $\mathcal{O}(k)$
- Adding n rules leads to $\mathcal{O}(n^2)$ behavior ouch
- Userspace iptables(8) still submits entire tables, but translation process does currently add one rule at a time to xt2 however
- Important to keep in mind for future fine-grained modifications initiated from userspace

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• Insertion of rules can be batched; reservation of enough bytes at once:

Multi-rule reservation also in $\mathcal{O}(k)$

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new = malloc(cur_size + x);
memcpy(new, cur_ruleset, ins_offset);
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- Largest contiguous block is the set of rules of a chain
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- Therefore, with c chains, a bulk update would only be $\mathcal{O}\left(c\cdot n\right)$
- Still suboptimal: Consider low rule density from earlier: $\frac{n}{c} \rightarrow 1 \Longrightarrow \lim_{c \rightarrow n} \mathcal{O}(c \cdot n) = \mathcal{O}(n^2)$

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Indirect chain lookup

next_rule = tbl->blob +

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- (cf. Xt1: next_rule = tbl->blob + rule->jump_offset)
- On rule insertion/deletion, only chain_offset needs to be adjusted, for $\mathcal{O}\left(c\right).$
- Still has other costs: chain head deletion is $\mathcal{O}(k)$ (can be mitigated by lazy deletion).

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Blobs for ¥1,000: Decoupled chains

- Prediction/Assumption: Since jumps can go across the entire blob, D-cache won't help anyway
- Loosen up on strict packing, just a little

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Blobs for ¥1,000: Decoupled chains

- Prediction/Assumption: Since jumps can go across the entire blob, D-cache won't help anyway
- Loosen up on strict packing, just a little
- Let largest contiguous entity be the chain rather than table
- Combined with indirect chain lookup, no chain offset updates needed *at all*.

xt2 sample chain head

```
struct xt2_chain {
    char name[XT_EXTENSION_MAXNAMELEN];
    void *rule_blob;
};
```

Jump action

```
struct xt2_packed_etarget *target;
next_rule = target->r_jump->rule_blob;
```

 &some_xt2_chain always remains the same over its lifetime – no more updates of rules required

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Results

• 100k rules like before, measuring RTT again

Testing RTT for proto4 ping6 -Ac 500 ::1

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ping6 -Ac 500 ::1

- Observed expansion: $1.83 \times (xt1: 40.477 \text{ ms} \rightarrow \text{proto4}: 74.135 \text{ ms})$
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- Splendid! Packed-chain rulesets work.
- But what's with the remaining 83%?

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Rule counters in Xtables2

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- Per-rule counters are temporarily implemented by using two xt_quota ematches in upcounting mode
 - The "ipv6" match with -s ::1 -d ::1 runs in 200–300 cycles
 - One "quota" ematch takes prohibitely costly 4500 cycles
 - (In)significance of raw cycle counts
 - Does not tell whether PCR might still incur a bottleneck
 - Main function of xt_quota is only 19 LOC, but xt_ipv6's is 79 LOC.

Equal-power comparison

Just as costly		
-A INPUT -s ::1	d ::1 -m quotagrow -m quotagrow	

• Driving xt1 with xt_quota counters yields an RTT of 77.373 ms.

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Equal-power comparison

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- Driving xt1 with xt_quota counters yields an RTT of 77.373 ms.
- Xtables2 PCR (74.135 ms) is absolutely on par
- xt_quota is the one and only bottleneck

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xt_quota analysis

 Using the simplest possible counter implementation instead of full-featured xt_quota, proto4 execution time drops to 44.254 ms.

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xt_quota analysis

- Using the simplest possible counter implementation instead of full-featured xt_quota, proto4 execution time drops to 44.254 ms.
- Adding a kmalloc for a private data structure to this simple impl. and time jumps to 50.733 ms (= +15%).
- D-cache misses again!?

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Conclusion

Section TOC

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Future

Roadmap:

• Continue using packed rulesets for packet processing Deemed solvable:

• Optimize extensions to contain fewer far-away accesses Deemed infeasbly solvable:

ebtables

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Questions

• I know you have some!

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Questions

- K7: AMD K7 Athlon 1.66GHz (manuf. 2003) 256K cache 2.6.36
- i7: Intel Core i7 920 4-core 2.67GHz (2009) 8MB 2.6.33
- VM: VirtualBox machine 1-core on i7 2.6.36

Driver	RTT K7	RTT i7	RTT VM
xt1 + 2s	40.447	2.83	3.08
xt1 + 1Q	58.882	5.18	11.47
xt1 + 2Q	77.373	11.50	21.00
xt2-proto3 +2Q	113.424	n/a	24.47
xt2-proto4 +2Q	74.135	n/a	21.79
xt2-proto4 +2s	44.254	n/a	n/a

- s: simple local counters
- Q: xt_quota-based counters

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